Numerical Estimation of Thermal Analysis for an Electronic Package

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Abstract- Numerical estimation for an electronic package has been done by the lumped modeling method and verified the results along with some commercial tools such as coolit PCB (Cooling of Printed Circuit board Software). Electronic packages which are used in electronic industries have vital role in day to day life from a small constitute such as electronic chip or components as resistor to an electronic package as combination of components on a PCB (Printed Circuit Board) situated in a box. These components dissipate heat on the layered circuit board which damages the components as well as damages the package itself so that it cannot function well. The protection of Electronic components from thermal damages requires, can be done by careful selection of layouts, dissipation levels and thermal control methods. Coolit PCB tools for thermal analysis have widely used in the PCB industries which are used to get thermal temperature for board as well as for package levels. The results obtained through these tools are compared with the results given by the ISAC (Indian Space and Aerospace center). The conduction, convection and radiation modes has been considered under typical operating conditions, to improve the temperature for the electronic packages and based on the obtained results changes are incorporated and suggested for the improvement of electronic packages thermal performance.

Index Terms- NISA- Numerically integrated element for system analysis1, ITAS (Integrated thermal analysis simulator.

1. INTRODUCTION

Electronic packages have wide application in aerospace industry within the enclosure components like printed circuit board (PCB), Diodes resisters and capacitors are incorporated, main function is to predetermine temperature inside the enclosure. Thermal analysis carried out to evaluate the heat transfer rate inside the enclosure by considering the effect of conduction and radiation on electronic components mounted inside the enclosure.

In an electronic enclosure the different task is to maintain the temperature in electronic component within the specified range. Heat energy is liberated by the individual components in an enclosure and dissipated to the surroundings. While performing Thermal analysis on PCB board and other components for cooling purpose consider the combined effect conduction and radiation by describing the mathematical model for simulation purpose.

Printed circuit board shown in the Fig.1 PCB board is supports mechanically to the electronic components mounted on it and connects them electrically by using conductive substance like solder, pads. There are two methods of components mounted on the PCB board, previously components was mounted by drilling the PCB board. It reduces the strength of the PCB board by drilling number of small holes on it and it is difficult locate small components to the drilled hole.

The newer technic uses, solder and flux are coated over the PCB board in a specified track and components are mounted on the track.



Fig.1 PCB Board

Printed circuit board supports the components mounted on it connects them electrically by using conductive substance like solder and flux.

2. NOMENCLATURE OF PCB

3. DESIGN OF PRINTED CIRCUIT BOARD

Thermal Analysis is the analysis of thermal energy distribution on the card and thereby determining the hot spot on the board. This will help in arriving at a suitable thermal relief strategy to keep the card in safe operating temperature

3.1. Simulation settings for thermal analysis

Table 1 shows Simulation settings for thermal analysis

Table 1. Simulation settings for thermal analysis

Ambient temperature	71° Celsius (worst	
Incoming Air pressure	0 atmospheric	
Gravitational force	0	
Default height of the components	4mm	
Number of Iterations	100	

3.2. Enclosure properties

Enclosure Properties	
Size (mm)	Boundary Conditions
dz 🔢 🗆	Type: Custom 💌
d <u>Y</u> 146 🗆	X min: Wall Properties
dZ 19 □	X max: Wall Properties
- Gravity Vector (<u>m/sec^2</u>) g_x 0	Y min: Wall Properties
<u>av</u> 0	Y max: Wall Properties
<u>g_z</u> 0	Z min: Wall Properties
-Medium Type Air C Vacuum	Z max: Wall Properties
OK	Cancel Help

Fig.2 Enclosure properties

Fig 2 shows the enclosure properties in which size dx, dy and dz are fed into software and all values are in

mm. gravity vector in all the direction is set to zero, the working medium is specified as air. The minimum and maximum boundary conditions are entered into software with wall properties.

3.3. Board properties

rinted Circuit Board						
Properties Components						
Dielectric Material FR4	▼ Power W	0 Transient	Emissivity 0.9			
- Thermal Conductivity						
Calculate using layers		In-plane (<u>w//m*K)</u>	57.7655 Material			
C Enter manually		Through-plane (<u>W//m*K)</u>	0.401381			
Lavers						
Layer Name	Material	Thickness (mm)	% Coverage 🔺	Add layer		
Top	Copper (Pure)	0.0340	93.8400	Import DXF		
Dgnd	Copper (Pure)	0.0340	96.5225	Bemove		
🔲 Sig1	Copper (Pure)	0.0340	28.5100			
🔲 sig2	Copper (Pure)	0.0340	19.7625	Remove All		
3.3_V	Copper (Pure)	0.0340	94.8400			
Slg3	Copper (Pure)	0.0340	21.2425			
Sig4	Copper (Pure)	0.0340	16.5775			
1.2V	Copper (Pure)	0.0340	94.3700			
🔲 sig5	Copper (Pure)	0.0340	12.5600 💌			
Denvity (<u>b/b^*)3</u> 145.265 Hest Capacity (<u>J/kot/S</u>) 888.434 Initial Temperature (C) 55						
Cancel Apply Help						

Fig.3 Board properties

The printed circuit board designed will possess some properties in it, which will affect the board life. It is necessary to find out which components will produce more temperature and which components produce less temperature and then we can conclude where to give more cooling effect. The above figure indicates the board properties material, thickness percentage coverage with the components designed for this study.

3.4. Floor Planning of SAMPLE PCB Card

Fig. 4 shows the placement of major components on the SAMPLE PCB Card. This shows the placement on the component side of the card like microprocessor, capacitor, resistor, LED and switch. While planning space utilization has to be the main concern.



Fig.4 Top Placement side of the SAMPLE PCB Card.

3.5. Placement of board inside the enclosure



Fig.Error! No text of specified style in document. Board Placement inside the enclosure

After placement of electronic components on the board the top view, side view, front view and a 3D view of the board are shown in Fig 5. This enclosure is used for the thermal analysis on the board. It can be seen from the figure that the heat sink is through convection mode since no additional heat absorbing components are placed on the board.

4. THERMAL POWER ANALYSIS

- 4.1 Thermal power distribution analysis:
 - a. Temperature analysis of critical components b.Temperature distribution analysis on the card

4.2 Temperature analysis

This analysis gives us graphical representation of temperature of each critical component. These are shown in the Table 2.

Name	T min (°C)	T max (°C)	T avg (°C)
J4 - (BERGSTRIP(3- PIN) 4.00)	74.5171	75.2668	74.8561
D2 - (1N4148 1.70)	72.6346	72.7184	72.6817
R7 - ((1206) 4.00)	74.9715	75.4894	75.2327
R75 - ((1206) 4.00)	72.971	73.1774	73.0703
U4 - (ADM8697 2.65)	75.0447	75.7894	75.4322
R49 - ((1206) 4.00)	72.5454	72.7646	72.6608
R99 - ((1206) 4.00)	72.7705	72.9512	72.8643
C156 - ((0805) 4.00)	72.5609	72.8727	72.7028
D8 - ((DO-35) 2.28)	73.399	73.5913	73.4906

Table 2. The actual temperature of all the components

5. RESULTS AND DISCUSSION

5.1. Temperature Distribution Graphs



Fig.6 Graphical Representation of board temperature at top placement side

The maximum temperature of 91.370C is found on the board on the place where processor is placed. The temperature found is high this temperature has to be removed from the source, if this temperature is not removed it will spoil the life of the processor. The temperature generated on this part of PCB board is spread throughout the surrounding of the board which can be seen from the plots. The temperature of around 830 C is found on the other part which is near to processor, if processor is cooled chip whose temperature is 830 C will also reduce, this is because the temperature travelling from the processor will reduce. The temperature on the other two chips is found around 760C will be less damaged since least temperature on the board is 710C.







Fig.8 Representation of board temperature at Bottom placement side



Current cross-section position is Z = 5.77268 mm Display 'T' scalar field Component surfaces are painted with 'T'

Fig.9 3D Graphical Representation of board temperature from Bottom side.





Fig.10 Graphical Representation of board Heat flux at top placement side



Current cross-section position is Z = 8.02634 mm. Display 'I q I' scalar field and 'q' vectors painted with 'I q I' Component surfaces are painted with 'I q I'

Fig.11 3D Graphical Representation of board Heat flux from top placement side







Fig.13 Enclosure, control card and power card



Fig.14 Result obtained by NISA for Verification Problem



Fig.15 Schematic showing the temperature distribution in an Enclosure



Fig.16 Result Comparison Graph

6. CONCLUSIONS

Numerical estimation of thermal analysis for electronic components is concerned with the predicating the temperature of electronic package under specified heating environment. The electronic package existed on various electronics industries in various form for example CPU, UPS, Cellphone etc. experiences the thermal load. In this the typical package consists of a set of PCB card on which various electronics components mounted on it. The thermal analyses of these electronic packages provide the lifespan of components and packages, the design and analysis of these types of packages are a futile task. To define a good method of cooling or protecting this package, it is required to estimate the temperatures of different components used, traces of the board and the radiation energy between different boards and between boards to packages. An accurate estimation of thermal design and analysis solver development can gives an ease in thermal analysis of an electronic packages and its components with the analyst an idea to estimate the design alternatives for estimation of better thermal management process. Thermal performance levels as temperatures can be obtained under different operating conditions and safe operating levels can be defined for operation of any electronic package.

An electronic package with typical PCB card with several components mounted on it has thermally analyzed using the tools Coolit PCB and the results has verified with the Indian Space Satellite Center results. The analysis carried out using Coolit PCB could help into the thermal behavior of an electronic package.

7. SCOPE FOR FUTURE RESEARCH

The numerical analysis provides an insight which reduces experimental costs and increases reliability for components and a better thermal design and analysis can be obtained, hence in future the demands for this analysis will be reduced for reducing laborious experimental work and to reduce cost.

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